AGENDA FOR 27 SEPTEMBER 2010 INTERVIEW (4 P.M. EDST) FOR U.S. 10/684,053 WITH EXAMINER HUISMAN

1)   Section 16 of the office action – Inventors John Rapp and Scott Hellenbach will explain how an embodiment described in the application works without generating data-destination information.

2)   Section 43 of the office action – we will explain why combination of Dretzka and Chamdani do not obviate claims 1-3, 5-9.

3)   Section 52 et al. – John Rapp and Scott Hellenbach will explain why one of skill in the art would not thread Dretzka's process, and why the examiner's stated advantage of reducing stall time is irrelevant to Dretzka's process.

4)   Sections 57(e), 67(h), et al – we will refute examiner's interpretation of Mr. Rapp's declaration, and discuss in general whether examiner is saying that he does not believe Mr. Rapp's factual declaration, and if he has facts to support an opposite conclusion.

5)   Section 69 – discuss why claims 51-52 are not obvious in view of Dretzka and newly cited Gilson.

6)   Section 73 – discuss examiner's disagreement with 132 declaration

7)   Sections 75-76 – point out why examiner's conclusion re Dretzka has no support in Dretzka.

8)   Section 77 – ask examiner to explain how a wire/bus processes data.

9)   Section 78 – See (3)

10)   Section 79-80 – Explain why transfer objects for buffers are not inherent in Dretzka.